

(SYLLABUS)

1.

(Course Title)	EDA Full-custom	(Instructor)			
(Year)	2023	(Semester)	2	(Course No.)	2150085301
(Class)	01	(Open to)	3, 4	(Course Classification)	-
(Credit)	3.0		03		100
(Office)	203	(Telephone)	02-829-8227	(e-mail)	sohnbb0324@ssu.ac.kr
	(PBL)		+		2023
	(*) (ABEEK Classification)		(*) (ABEEK Requirement)		
(Course Description)	Full-custom Layout Simulation Layout				

<div><div>○ Full-custom</div><div>○ Full-custom Layout</div><div>○ Full-custom</div></div>	

가	(100)	(100%)
	10	10
	60	60
	30	30

(Required Texts)		
	()	* /EDA Full-custom /2023

2.

(Week)	(Keyword)	(Description)		(Texts)
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01	Full-custom	<ul style="list-style-type: none"> ○ ○ Full-custom 		
02	Full-custom	<ul style="list-style-type: none"> ○ Full-custom ○ 	,	
03	NMOS/PMOS	<ul style="list-style-type: none"> ○ NMOS ○ PMOS 	,	
04	Inverter/NAND	<ul style="list-style-type: none"> ○ Inverter ○ NAND 	,	
05	NOR/MUX	<ul style="list-style-type: none"> ○ NOR ○ MUX 	,	
06	Amplifier	○ Amplifier 1	,	
07	Amplifier	○ Amplifier 2	,	
08		<ul style="list-style-type: none"> ○ ○ 1~7 	,	
09	Layout Editor, Physical Verification(DRC, LVS), Design Rule	<ul style="list-style-type: none"> ○ Layout Editor ○ Design Rule ○ Physical Verification(DRC, LVS) 		
10	Inverter Layout Design, NAND Layout Design	<ul style="list-style-type: none"> ○ Inverter Layout Design ○ NAND Layout Design 	,	
11	NOR Layout Design, MUX Layout Design	<ul style="list-style-type: none"> ○ NOR Layout Design ○ MUX Layout Design 	,	
12	Amplifier Layout Design	<ul style="list-style-type: none"> ○ Amplifier Layout Design ○ Physical Verification 	,	
13	RC Extraction, Post-Simulation	<ul style="list-style-type: none"> ○ Inverter/NAND/NOR RC Extraction(LPE) ○ Inverter/NAND/NOR Post Simulation 	,	
14	RC Extraction, Post-Simulation	<ul style="list-style-type: none"> ○ MUX/Amplifier RC Extractin(LPE) ○ MUX/Amplifier Post Simulation 	,	
15		<ul style="list-style-type: none"> ○ ○ 9~14 	,	

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3. ()

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	Open-ended problem		
	Teamwork		
	Communication skills		